Htno	Subcode	Subname	Internal	External	CREDITS
15991A0408	RT22022	SWITCHING THEORY AND LOGIC DESIGN	19	-1	0
15991A0408	RT22041	ELECTRONIC CIRCUIT ANALYSIS	18	-1	0
15991A0408	RT22042	RANDOM VARIABLES & STOCHASTIC PROCESSES	16	-1	0
15991A0408	RT22044	EM WAVES AND TRANSMISSION LINES	22	-1	0
15991A0408	RT22045	ANALOG COMMUNICATIONS	25	-1	0
15991A0422	RT22042	RANDOM VARIABLES & STOCHASTIC PROCESSES	18	12	0
15991A0422	RT22044	EM WAVES AND TRANSMISSION LINES	18	49	3
15991A0426	RT22042	RANDOM VARIABLES & STOCHASTIC PROCESSES	19	13	0
15991A0508	RT22052	JAVA PROGRAMMING	23	29	3
15991A0508	RT22053	ADVANCED DATA STRUCTURES	19	15	0
15991A0508	RT22054	COMPUTER ORGANIZATION	20	35	3

**Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 18-03-2023]

Imp Note:

- * -1 in the filed of externals indicates student is absent for the respective subject.
- * -2 in the filed of externals indicates student result Withheld for the respective subject.
- * -3 in the filed of externals indicates student involved in Malpractice for the respective subject.

Date:13.03.2023

N. Ratic

Controller of Examinations(UG)